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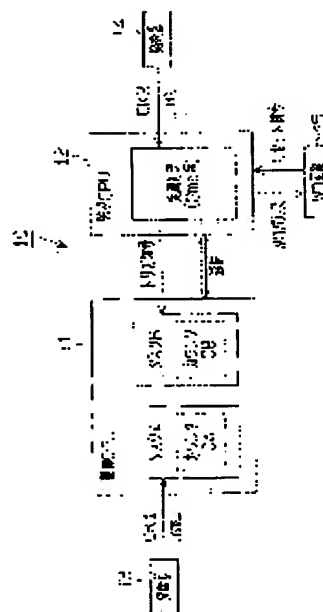
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(54) ELECTRONIC CONTROL UNIT

(57)Abstract:

PROBLEM TO BE SOLVED: To properly detect clock abnormality and process omission abnormality and then to secure the reliability of a CPU.

SOLUTION: An engine ECU 10 is equipped with a control CPU 11 and a monitor CPU 12. The monitor CPU 12 generates a trigger signal having a fixed period according to a clock inputted to itself and sends it to the control CPU 11. The control CPU 11 actuates a task A in fixed cycles on the basis of a clock inputted to itself to make a counter CA count and also actuates a task B on the basis of the trigger signal inputted from the monitor CPU 12 to make a counter CB count. The differences between the counters CA and CB are compared with each other at two successive timing points and clock abnormality is detected according to the comparison result.



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